

APPLICATION DATA SHEET

APPLICATION INFORMATION

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| Application Type:: | Regular |
| Subject Matter:: | Utility |
| Title:: | Variable Stage Ratio Buffer Insertion For Noise Optimization In A Logic Network |
| Attorney Docket Number:: | 07716P001 |
| Request for Early Publication?:: | No |
| Request for Non-Publication?:: | No |
| Total Drawing Sheets:: | 4 |
| Small Entity?:: | No |
| Petition included?:: | No |
| Licensed US Govt. Agency:: | No |
| Contract or Grant Numbers:: | No |
| Secrecy Order in Parent Appl.?:: | No |

APPLICANT INFORMATION

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|---|---------------|
| Applicant Authority Type:: | Inventor |
| Primary Citizenship Country:: | US |
| Status:: | Full Capacity |
| Given Name:: | Kenneth |
| Middle Name:: | Hing Key |
| Family Name:: | Tseng |
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| State or Province of Residence:: | CA |
| Country of Residence:: | US |
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CORRESPONDENCE INFORMATION

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REPRESENTATIVE INFORMATION

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| Representative Customer Number:: | 27660 | |
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ASSIGNEE INFORMATION

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